

Certificate of Volatility PTG 1802

The purpose of this Certificate of Volatility is to describe the memory components used in the LYNX Technik PTG 1802 and to classify them as either volatile or non-volatile to support customer data-security and disposal or decommissioning assessments.

The PTG 1802 is a compact, battery-powered SDI video and audio test signal generator for verifying and testing SD/HD/3G-SDI infrastructures, which generates internally stored test patterns and embedded audio tones and outputs them via BNC (SDI/sync) connectors, without processing, storing, or transmitting any external customer video, audio or file data, since it only produces and outputs predefined internal test signals.

Component Analysis

The following table lists the memory-relevant integrated circuits together with their volatility classification.

Component	Type	Function	Volatility	Data Retained
IS61WV5128BLL-10BLI	Asynchronous SRAM (512K x 8)	Working/frame buffer memory for internal video/pattern processing	Volatile	None after power off
IDT71V35761S200PFG	Synchronous SRAM	Video signal buffer/FIFO for SDI pattern generation	Volatile	None after power off
FT245BL/TR (FT245BM)	USB FIFO controller	Charging control or firmware updates	Volatile	None after power off, no storage function
M25P64-VME6TG	Serial NOR Flash (64 Mbit)	Stores device firmware, internal test pattern library and system configuration (factory-programmed)	Non-Volatile	Firmware/pattern data only; no customer/user data
93C46B/SN	Serial EEPROM (1 Kbit)	Stores device/system configuration parameters (factory/calibration data)	Non-Volatile	Configuration data only; no customer/user data
AT89C51ED2-RLTUM	8051 Microcontroller with internal Flash	Executes device control firmware	Non-Volatile (internal program Flash)	Firmware only; no customer/user data
A3P030-VQG100	Flash-based FPGA	Logic/control functions; configuration stored in on-chip Flash	Non-Volatile (configuration Flash)	Bitstream/logic configuration only; no customer/user data
A3P600-PQG208	Flash-based FPGA	Video signal processing logic; configuration stored in on-chip Flash	Non-Volatile (configuration Flash)	Bitstream/logic configuration only; no customer/user data
DS28CM00R-A00+U	1-Wire Silicon Serial Number	Factory-programmed unique device identifier (read-only)	Non-Volatile	Factory device ID only; not user-modifiable, no customer data
GS2972-IBE3	SDI Serializer/Deserializer	Signal processing (no persistent storage)	Volatile (registers only)	None after power off
ADS7843EG4	Touch-screen ADC controller	Touch input digitization (no persistent storage)	Volatile (registers only)	None after power off

Summary and Conclusion

The PTG 1802 contains two categories of memory:

- **Volatile memory** (SRAM buffers, FIFO, and processing registers) is used exclusively for transient signal buffering during pattern generation and is fully cleared upon loss of power. No customer, classified or user data is ever written to these components as the device only generates internally stored test patterns.
- **Non-volatile memory** (SPI Flash, EEPROM, FPGA configuration Flash, microcontroller Flash, and the 1-Wire ID chip) stores only manufacturer-installed firmware, the internal test pattern library, calibration/configuration parameters, and a factory-assigned device identifier. None of these non-volatile memories stores, processes or retain any customer-supplied video, audio, or file data, since the unit does not accept or forward external content, it only outputs internally generated test signals.

The USB port on the PTG 1802 is used for battery charging or firmware updates. No data exchange with connected computers or external systems occurs during normal use and no customer data can be stored on or extracted from the device via this interface.

Statement

Based on the analysis above, the LYNX Technik PTG 1802 does not store, process or retain any customer-specific, classified or sensitive user data in either its volatile or non-volatile memory components. All non-volatile memory content is limited to manufacturer firmware, internal test patterns and factory calibration/configuration data.



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Place and date of issue

Legal Signature